

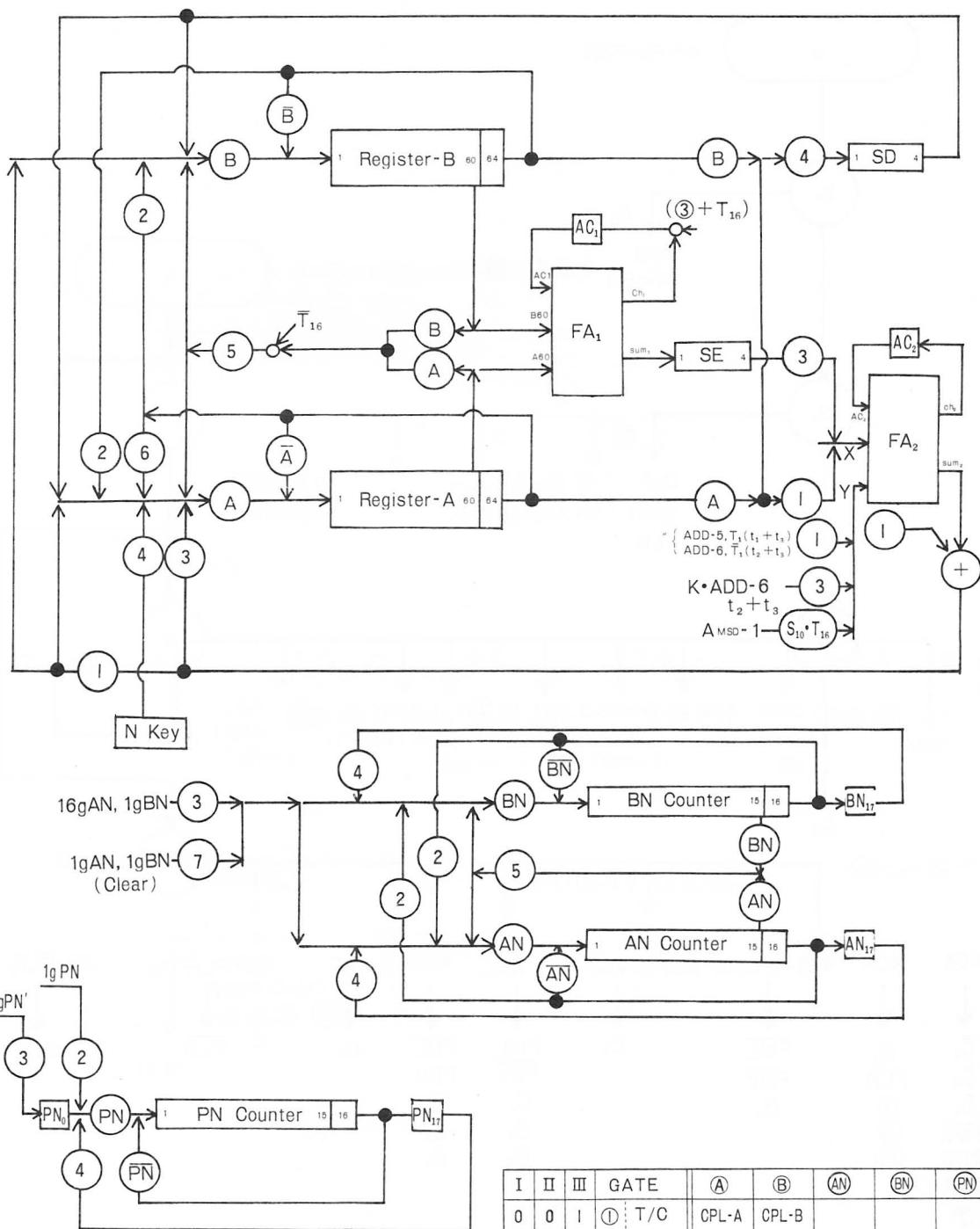


Model **ICC-0081**

ELECTRONIC MINI CALCULATOR

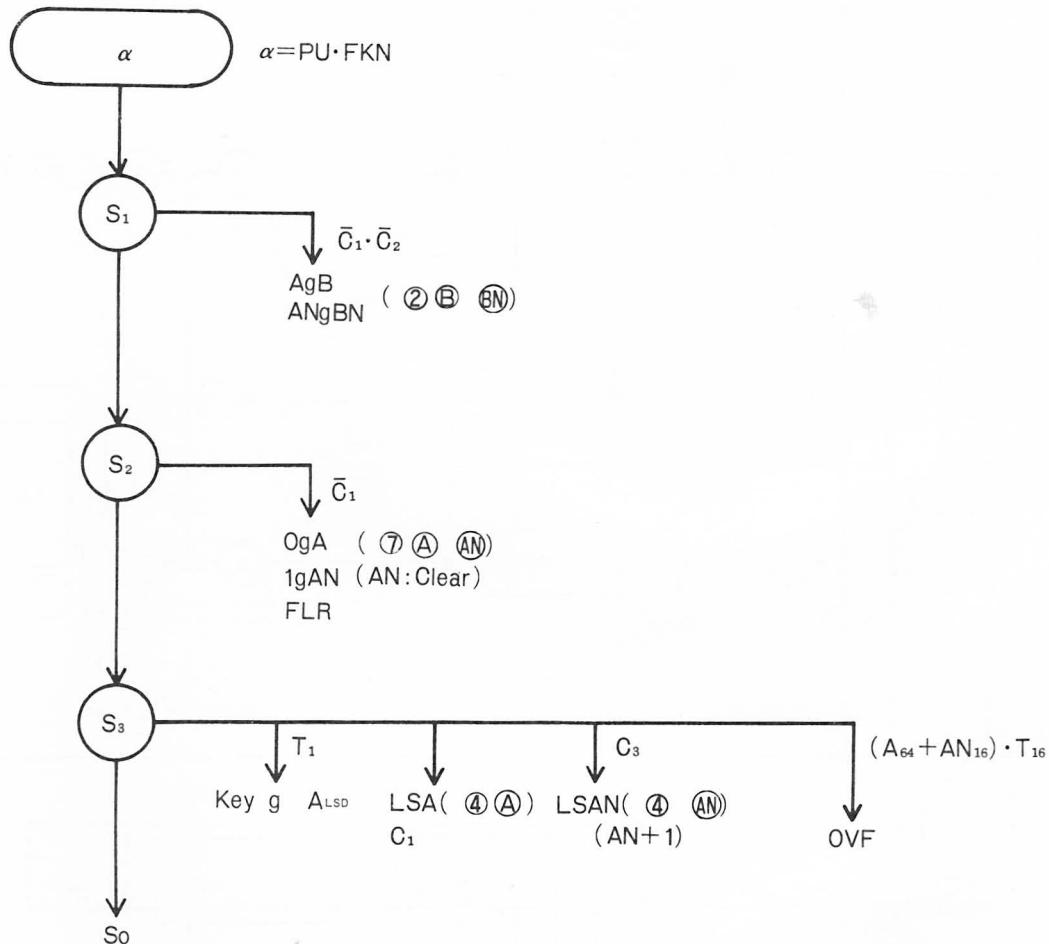
LOGIC CIRCUIT DIAGRAM

Block Diagram



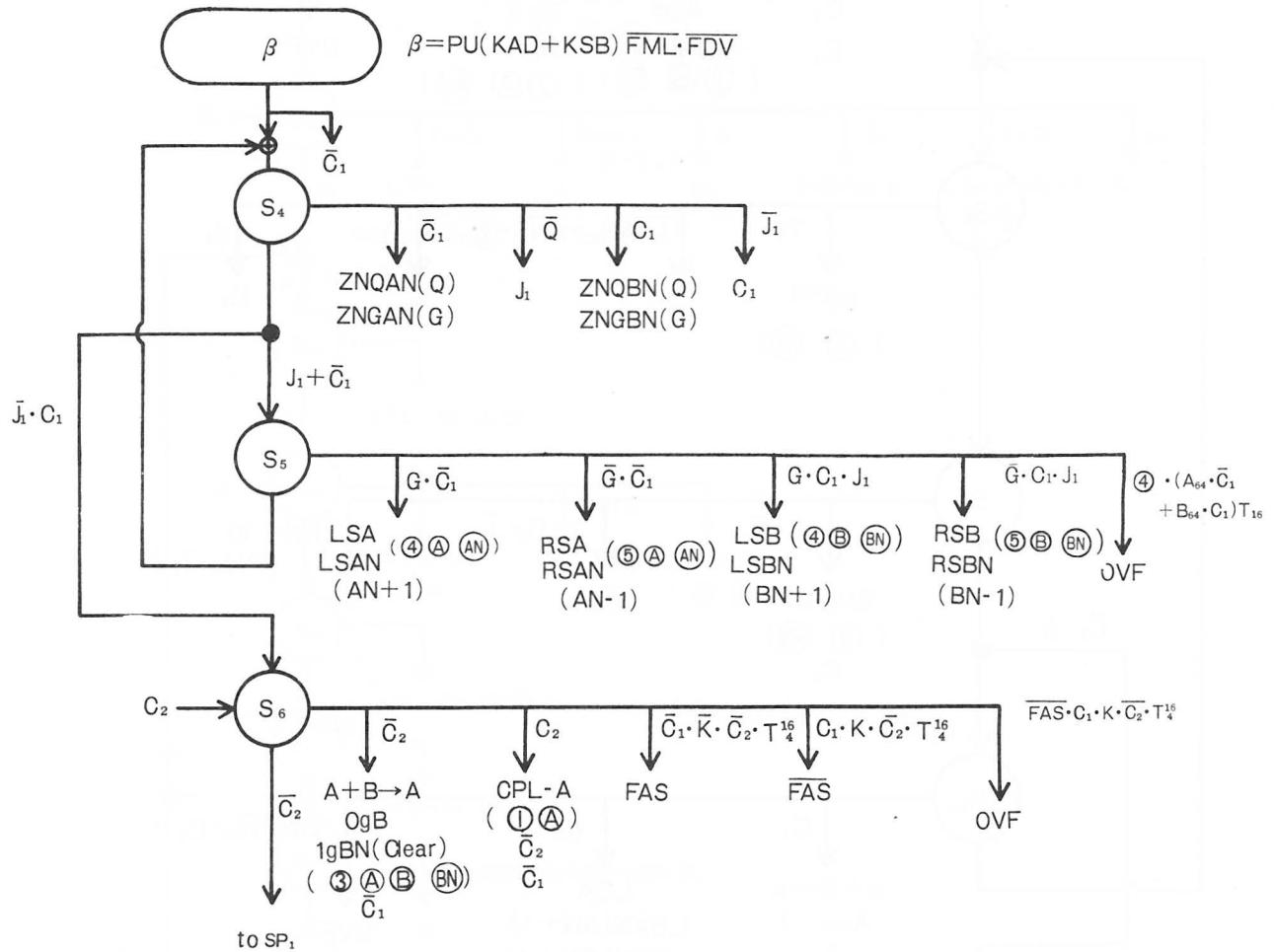
I	II	III	GATE	(A)	(B)	(AN)	(BN)	(PN)
0	0	I	① T/C	CPL-A	CPL-B			
0	1	0	② EXC.	Bg A	Ag B	BNg AN	ANg BN	1g PN
0	1	I	③ ADD	A+B→A	CLB	16g AN	1g BN	1g PN'
I	0	0	④ LS	LSA	LSB	LS AN	LS BN	LS PN
I	0	I	⑤ RS	RSA	RSB	RSAN	RS BN	
I	I	0	⑥ DR	Drop A				
I	I	I	⑦ CLR	Og A	Og B	1g AN	1g BN	Og PN

Input Sequence

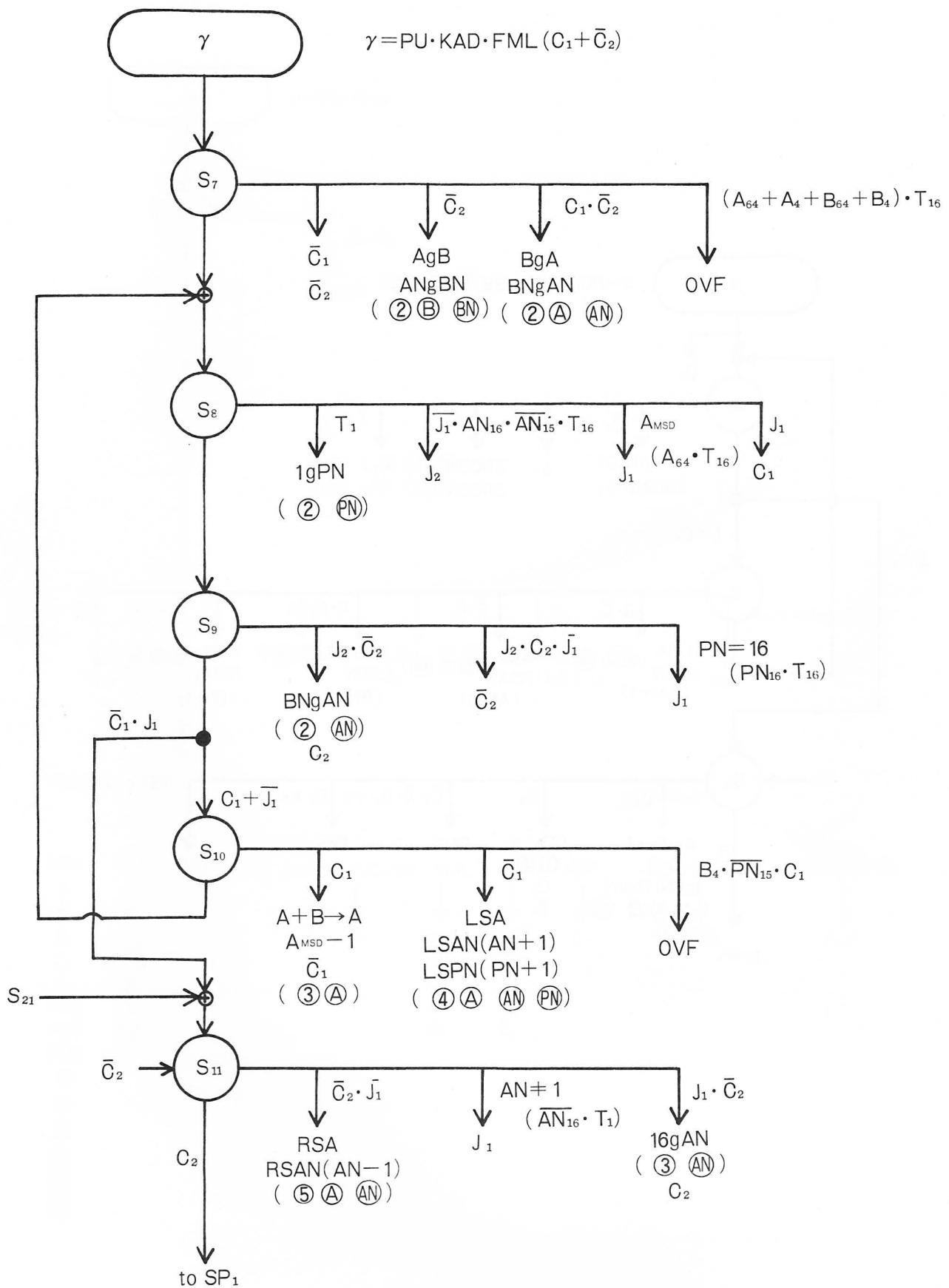


KCA	KCK	$KAD \cdot PU \cdot \bar{C}_1 \cdot C_2$	$KS_B \cdot PU \cdot \overline{FML} \cdot \overline{FDV}$	KML	KDV	KDP	$KD \cdot FLR \cdot PU$	$KD \cdot \overline{FLR} \cdot PU$
\bar{C}_1	\bar{C}_3	\overline{FML}	\overline{FDV}	C_2	\overline{FML}	\overline{FDV}	C_3	\overline{FLR}
\bar{C}_2	\overline{FLR}				\bar{C}_1	\bar{C}_1	\bar{C}_2	
\bar{C}_3	$\textcircled{7}$		\bar{C}_2			\bar{C}_3	\bar{C}_3	
\overline{FML}	\textcircled{A}							
\overline{FDV}	\textcircled{AN}							
\overline{FLR}								
$\textcircled{7}$								
\textcircled{A}								
\textcircled{B}								
\textcircled{AN}								
\textcircled{BN}								
\textcircled{PN}								

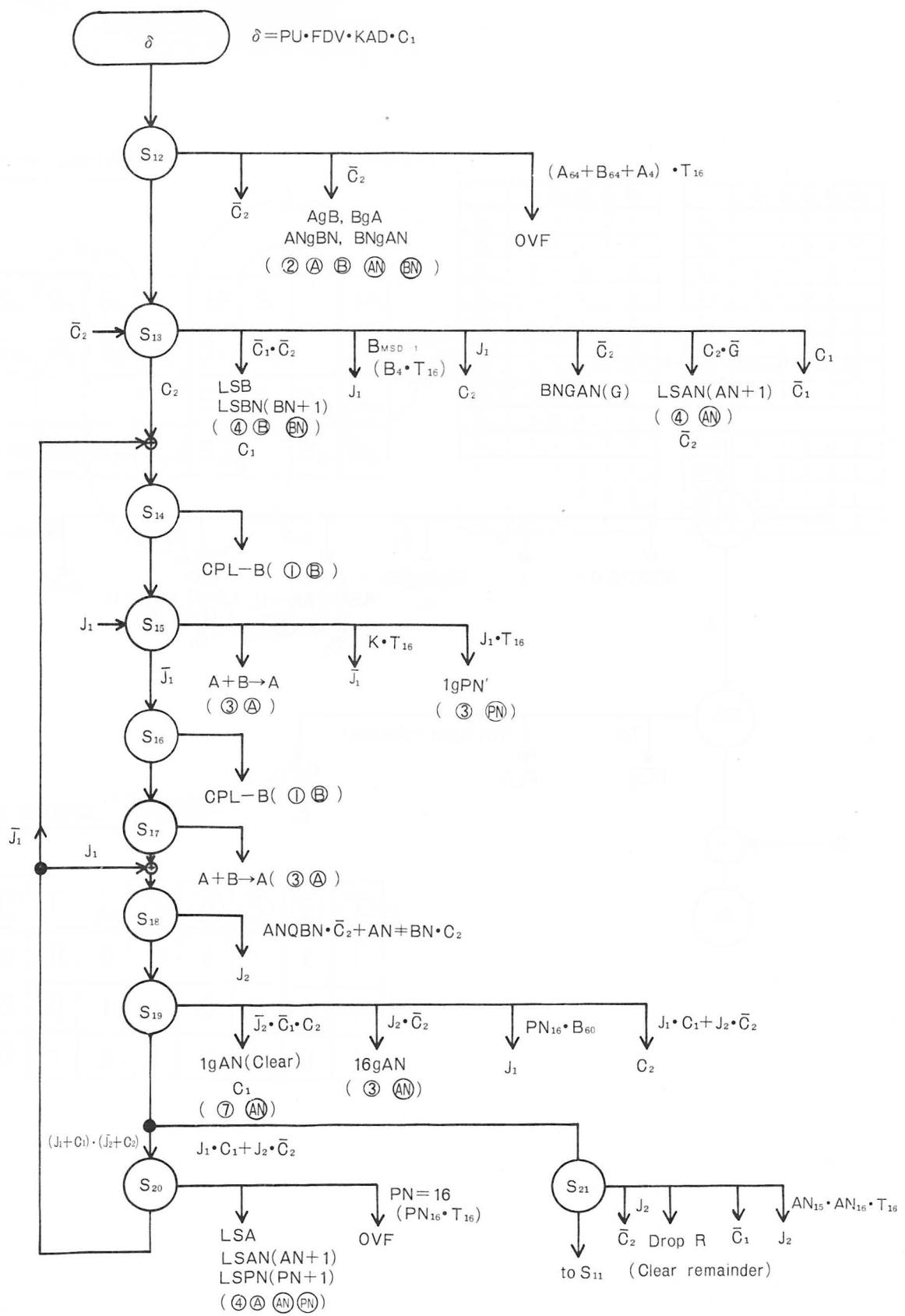
Add. Sub. Sequence



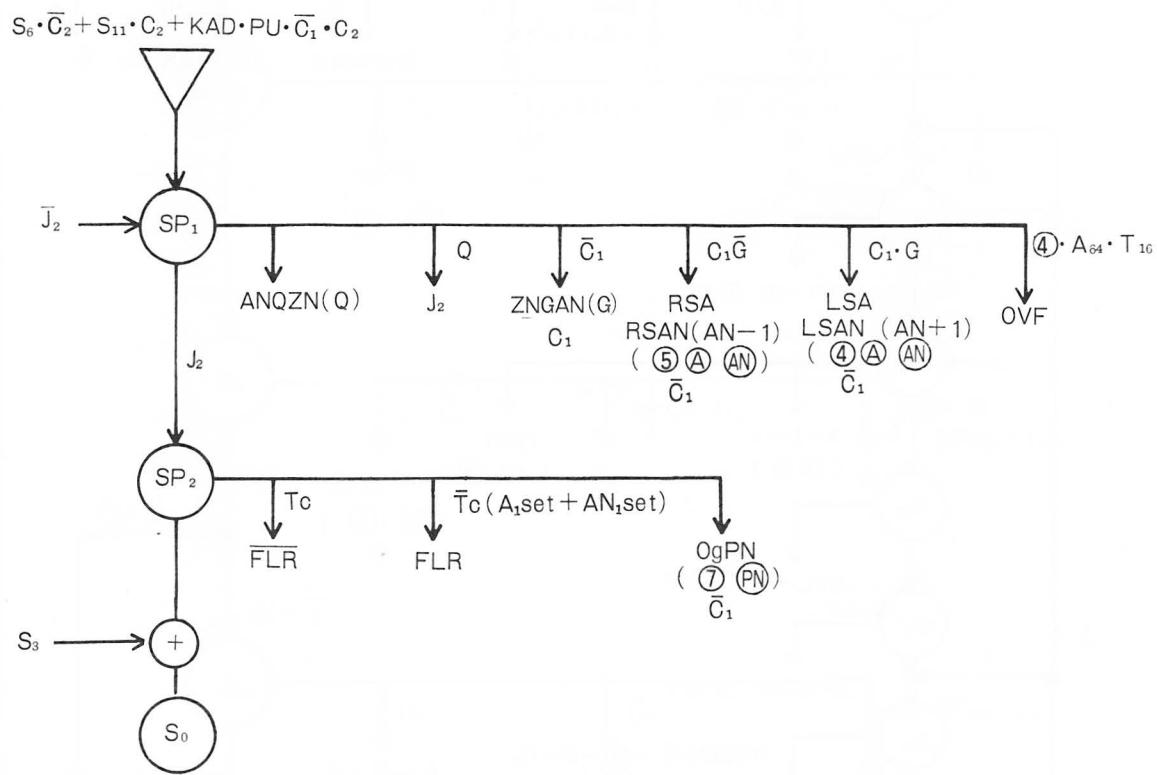
Mul. Sequence



Div. Sequence

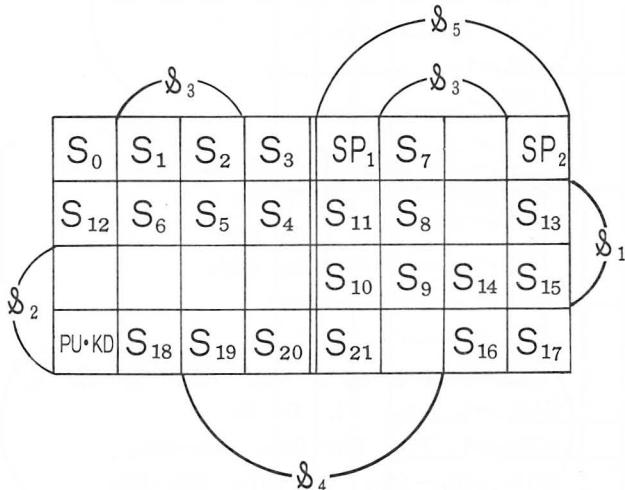


D. P. Fixing Sequence



Stage Assignment

Sequence control.



	S ₁	S ₂	S ₃	S ₄	S ₅
S ₁	0	0	1	0	X
S ₂	0	0	1	1	0
S ₃	0	0	0	1	0
S ₄	1	X	0	1	0
S ₅	1	X	1	1	0
S ₆	1	X	1	0	0
S ₇	0	0	1	X	1
S ₈	1	0	1	X	1
S ₉	X	1	1	1	1
S ₁₀	1	1	0	1	X
S ₁₁	1	0	0	1	1
S ₁₂	1	X	0	0	0
S ₁₃	1	0	X	0	1
PU•KD					
S ₁₈					
S ₁₉					
S ₂₀					
S ₂₁					
SP ₁					
SP ₂					
S ₀	0	0	0	0	0

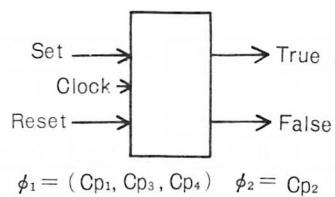
	S ₁	S ₂	S ₃	S ₄	S ₅
S ₁₄	I	X	1	0	I
S ₁₅	I	I	0	0	X
S ₁₆	0	X	1	0	I
S ₁₇	0	I	0	0	I
S ₁₈	X	I	I	0	0
S ₁₉	X	I	I	I	0
S ₂₀	X	I	0	I	0
S ₂₁	0	I	X	I	I
PU•KD	0	I	0	0	0
SP ₁	0	0	0	I	I
SP ₂	0	0	X	0	I
S ₀	0	0	0	0	0

Gate control.

	①	②	③	④	⑤	⑥	⑦
I	0	0	0	0	I	I	I
II	0	0	I	I	0	0	I
III	0	I	0	I	0	I	0

Notations

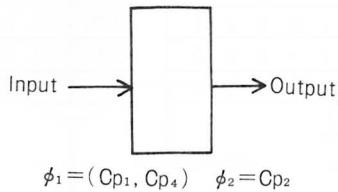
* S-R Flip Flop



$\text{Cp}_1 = \text{FLR}$
 $\text{Cp}_3 = \$_1, \$_2, \$_3, \$_4, \$_5, \text{C}_1, \text{C}_2, \text{C}_3,$
 $\text{FML}, \text{FDV}, \text{P}_1, \text{P}_2,$
 $\text{Cp}_4 = \text{Tc}$

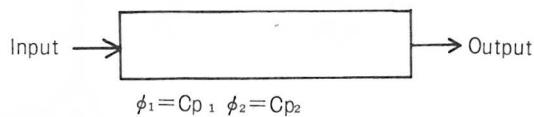
Clock : $\begin{cases} \text{Cp}_2 : \text{Common Clock Pulse} \\ \text{Cp}_1 : \text{Bit} \\ \text{Cp}_4 : \text{Digit} \\ \text{Cp}_3 : \text{Word} \end{cases}$

* Delay Flip Flop



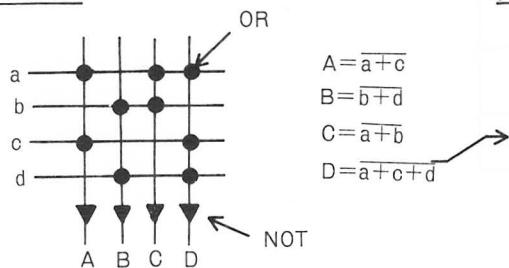
$\text{Cp}_1 = \text{FKN}, \text{J}_1, \text{J}_2, \text{G}, \text{AC}_1, \text{AC}_2, \text{T}_1^1 \sim \text{T}_4^1, \text{K},$
 $\text{t}_2, \text{t}_3, \text{t}_4, \text{T}_1^1, \text{D}_1 \sim \text{D}_3$
 $\text{Cp}_4 = \text{T}_1, \text{PNo}, \text{PN}_1 \sim \text{PN}_{17}, \text{BN}_1 \sim \text{BN}_{17},$
 $\text{AN}_1 \sim \text{AN}_{17}, \text{DT}_1 \sim \text{DT}_7, \text{DD}_1 \sim \text{DD}_4$

* Shift Register

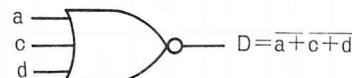


$\text{Cp}_1 = \text{A}_1 \sim \text{A}_{64}, \text{B}_1 \sim \text{B}_{64}$
 $\text{SD}_1 \sim \text{SD}_4, \text{SE}_1 \sim \text{SE}_4$

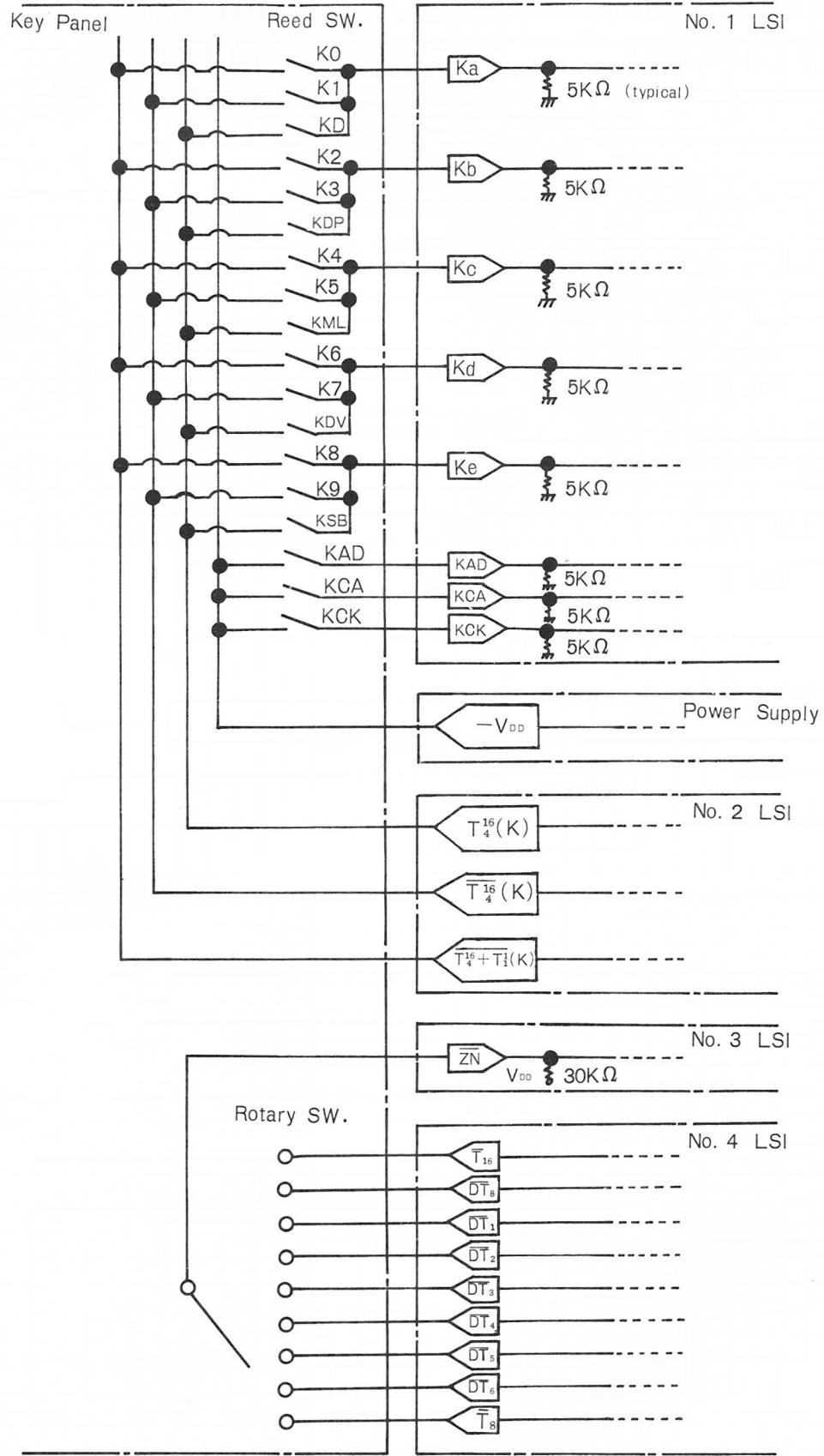
* Matrix



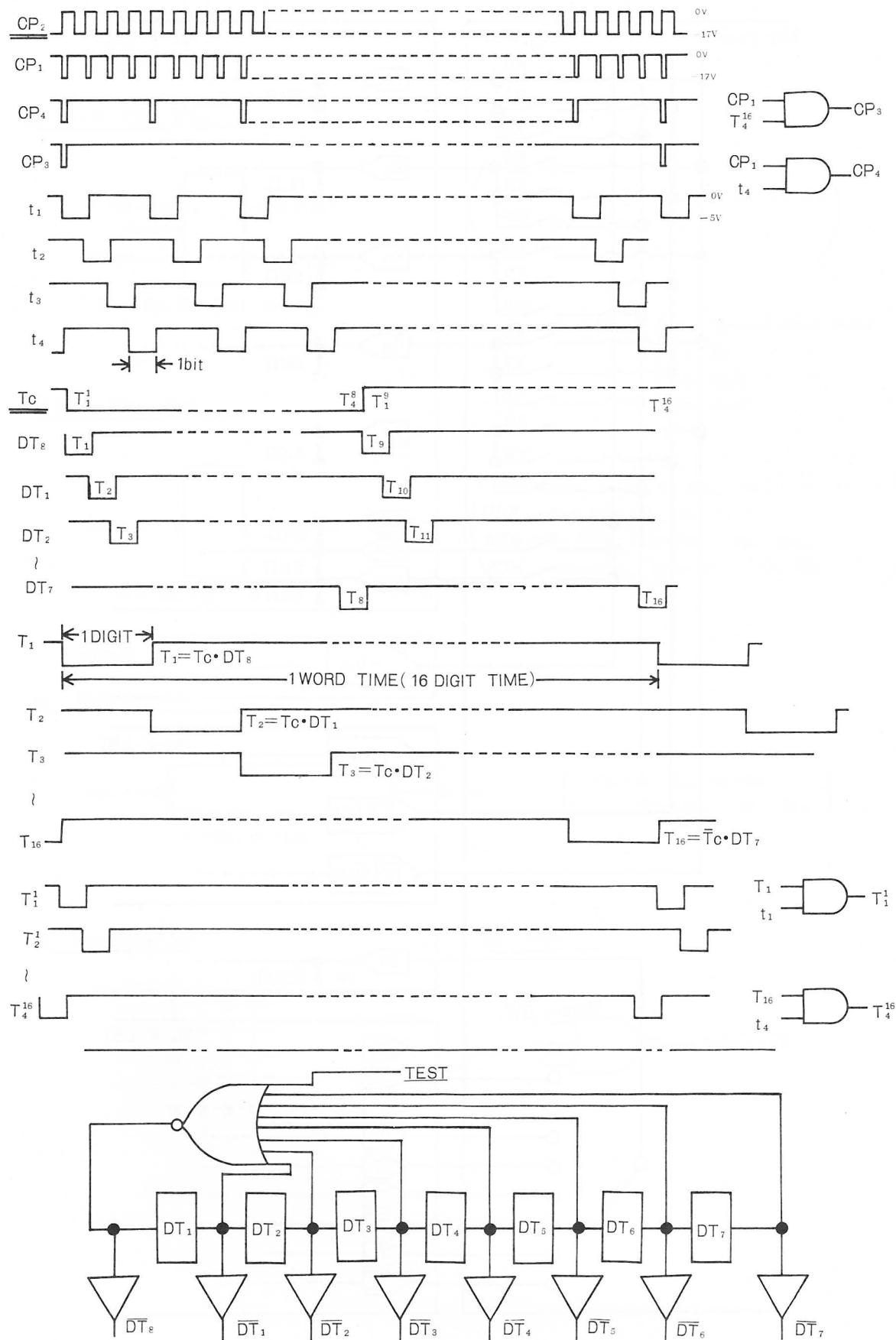
* NOR



Key Signal Circuit Schematic



Timing Pulse Diagram



Contents of Register

Contents of Counter AN BN PN

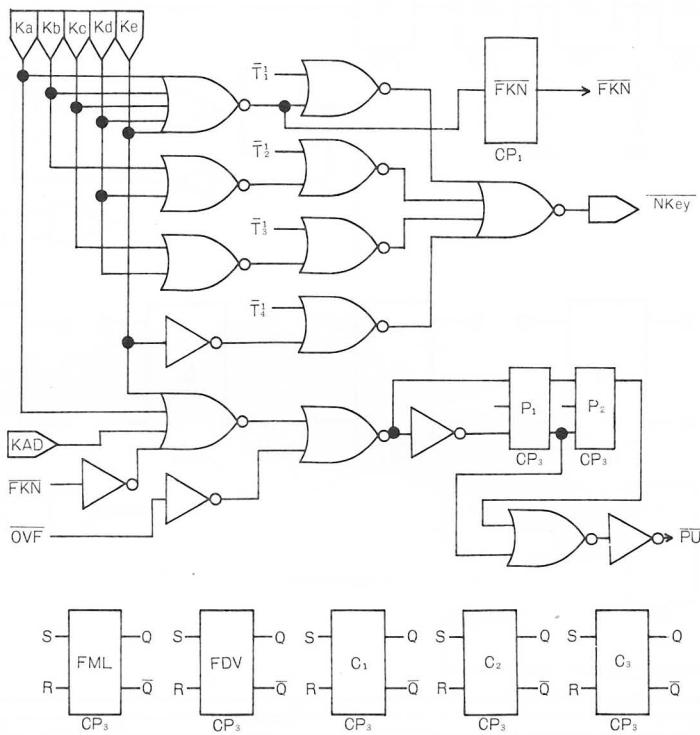
	T ₁	T ₂	T ₃	T ₄	T ₅	T ₆	T ₇	T ₈	T ₉	T ₁₀	T ₁₁	T ₁₂	T ₁₃	T ₁₄	T ₁₅	T ₁₆			
AN ₁ (BN ₁)	15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14			
AN ₂ (BN ₂)	14	15	0	1												13			
AN ₃ (BN ₃)	13	14	15													12			
AN ₄ (BN ₄)	12	13														11			
AN ₅ (BN ₅)	11															10			
AN ₆ (BN ₆)	10															9			
AN ₇ (BN ₇)	9															8			
AN ₈ (BN ₈)	8															7			
AN ₉ (BN ₉)	7															6			
AN ₁₀ (BN ₁₀)	6															5			
AN ₁₁ (BN ₁₁)	5														3	4			
AN ₁₂ (BN ₁₂)	4														1	2	3		
AN ₁₃ (BN ₁₃)	3														15	0	1	2	
AN ₁₄ (BN ₁₄)	2														13	14	15	0	1
AN ₁₅ (BN ₁₅)	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	0			
AN ₁₆ (BN ₁₆)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	1		
AN ₁₇ (BN ₁₇)	15	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		

B ₄	m	ℓ	2	3	4	5	6	7	8	9	10	11	12	13	14	15
B ₆₀	2	3	4	5	6	7	8	9	10	11	12	13	14	15	m	ℓ

PN ₀	1																		
PN ₁	16	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15			
PN ₂	15	16	1	2												14			
PN ₃	14	15	16													13			
PN ₄	13	14														12			
PN ₅	12															11			
PN ₆	11															10			
PN ₇	10															9			
PN ₈	9															8			
PN ₉	8															7			
PN ₁₀	7															6			
PN ₁₁	6														4	5			
PN ₁₂	5														2	3	4		
PN ₁₃	4														16	1	2	3	
PN ₁₄	3														14	15	16	1	2
PN ₁₅	2														12	13	14	15	1
PN ₁₆	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16			
PN ₁₇	16	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15			
	T ₁	T ₂	T ₃	T ₄	T ₅	T ₆	T ₇	T ₈	T ₉	T ₁₀	T ₁₁	T ₁₂	T ₁₃	T ₁₄	T ₁₅	T ₁₆			

L.S.I. No. 1 - 1

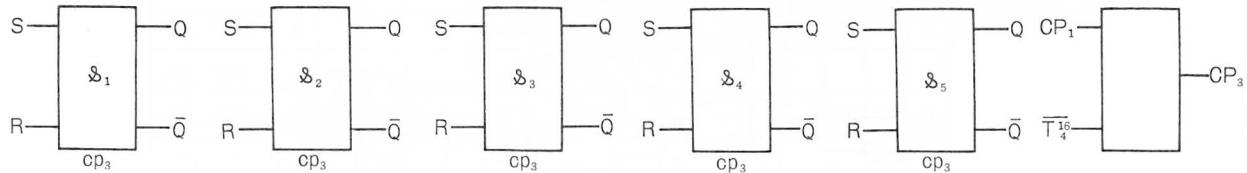
(FKN, NKey, PU, FML, FDV, C₁, C₂, C₃, S₁ ~ S₅, CP₁,
A, B, AN, BN, PN, I, II, III,



(Signal)	(Contents)
Ka	K ₀ + K ₁ + KD
Kb	K ₂ + K ₃ + KDP
Kc	K ₄ + K ₅ + KML
Kd	K ₆ + K ₇ + KDV
Ke	K ₈ + K ₉ + KSB
FML set	Kc
FML reset	KCA + Kd + KAD • PU • C ₁ • C ₂
FDV set	Kd
FDV reset	KCA + Kc + KAD • PU • C ₁ • C ₂
C ₁ set	S ₉ + S ₁₈ • C ₁ • C ₂ + S ₁₉ • C ₁ • C ₂ • J ₂ + S ₆ • J ₁ + SP ₁ • C ₁ + S ₄ • J ₁
C ₁ reset	KAD • PU • C ₁ • C ₂ + (KAD + Ke) PU • FML • FDV + OVF + Kc + Kd + KCA + S ₆ • C ₂ + S ₈ • C ₂ + S ₁₀ • C ₁ + S ₂₁ + SP ₁ • C ₁ • G + SP ₁ • C ₁ • G + SP ₂ + S ₇ + S ₁₃ • C ₁
C ₂ set	Ke • PU • FML • FDV + S ₉ • C ₂ • J ₂ + S ₁₁ • C ₂ • J ₁ + S ₁₈ • C ₂ • J ₂ + S ₁₉ • J ₁ + S ₁₉ • C ₁ • J ₁
C ₂ reset	KAD • PU • C ₁ • C ₂ + OVF + Kc + Kd + KCA + S ₂₁ • J ₂ + S ₆ • C ₂ + S ₁₃ • C ₂ • G + S ₁ + S ₆ • C ₂ • J ₁ + S ₁₂
C ₃ set	Kb
C ₃ reset	KCA + Kc + Kd + KCK + Ke • PU • FML • FDV + KAD • PU
P•U	P ₂ • P ₁
P ₁ set	OVF • (Ke + Ka + KAD + FKN)

(TOP VIEW)		I	PN	0	I3	J ₁	I	25	OVF	I
36		2	BN	0	I4	J ₂	I	26	CP ₂	I
35		3	AN	0	I5	G	I	27		
34		4	B	0	I6	Ka	I	28	CP ₁	I
33		5	A	0	I7	Kb	I	29	T ₁₈	I
32		6		0	I8	Kc	I	30	KCA	I
31		7		0	I9	Kd	I	31	KCK	I
30		8		0	I20	Ke	I	32	-V _{DD}	
29		9		0	I21	NKey	0	33	-V _{DC}	
28		10		0	I22	ALK TEST	0	34	III	0
27		11		0	I23	KAD	I	35	II	0
26		12		0	I24	V _{SS} (GND)	36	I		0
25		13		0						
24		14		0						
23		15		0						
22		16		0						
21		17		0						
20		18		0						
19										

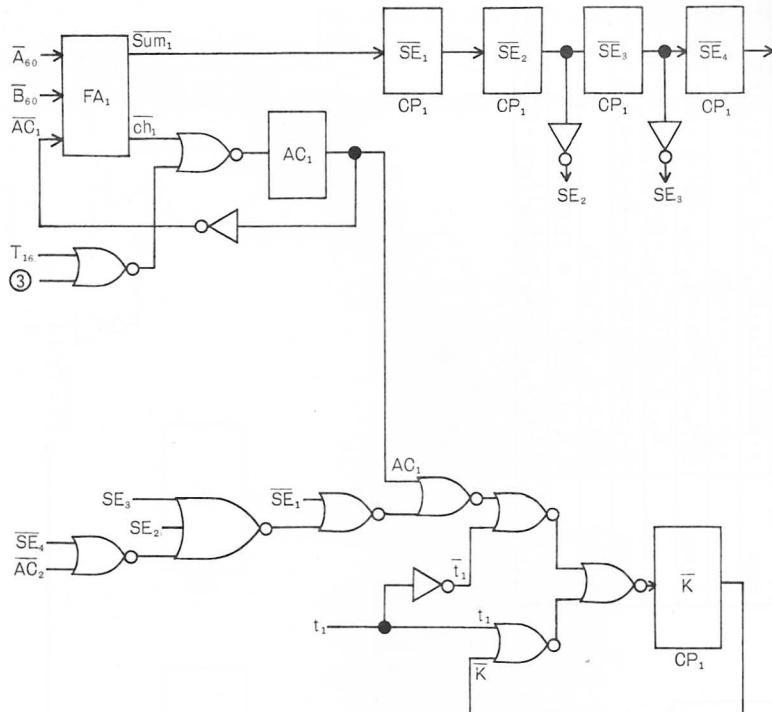
L.S.I. No. 1 -2



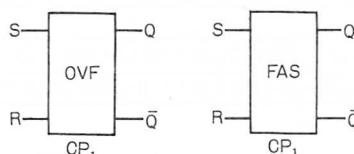
(Signal)	(Contents)
$\$_1$ set	$S_{20} \cdot \bar{J}_1 + S_7 \cdot S_{21} + (KAD + Ke) PU \cdot \bar{FML} \cdot \bar{FDV} + KAD \cdot PU \cdot C_1 \cdot FDV$
$\$_1$ reset	$S_{15} \cdot \bar{J}_1 + S_{11} \cdot C_2 + S_6 \cdot \bar{C}_2 + OVF + Kc + Kd + KCA$
$\$_2$ set	$S_{13} \cdot C_2 + S_8$
$\$_2$ reset	$S_{10} \cdot \bar{C}_1 + S_9 \cdot \bar{C}_1 \cdot J_1 + S_{21} + S_{10} \cdot C_1 + OVF + Kc + Kd + KCA$
$\$_3$ set	$S_{15} \cdot \bar{J}_1 + S_{13} \cdot C_2 + S_{19} \cdot \bar{C}_1 + S_4 + S_{20} + S_{17} + S_{10} \cdot C_1 + KAD \cdot PU \cdot FML(C_1 + \bar{C}_2) + FKN \cdot PU$
$\$_3$ reset	$S_9 + S_2 + S_5 + S_{19} + S_{14} + S_{16} + S_6 \cdot \bar{C}_2 + OVF + Kc + Kd + KCA$
$\$_4$ set	$S_1 + S_{18} + S_8 \cdot \bar{C}_2 + KAD \cdot PU \cdot FML(C_1 + \bar{C}_2) + (KAD + Ke) PU \cdot \bar{FML} \cdot \bar{FDV} + KAD \cdot PU \cdot \bar{C}_1 \cdot C_2$
$\$_4$ reset	$SP_1 \cdot J_2 + S_4 \cdot \bar{J}_1 \cdot C_1 + S_{20} + S_3 + OVF + Kc + Kd + KCA$
$\$_5$ set	$S_{20} \cdot \bar{J}_1 + S_{19} \cdot C_1 \cdot J_1 + S_{12} + S_{19} \cdot \bar{C}_2 \cdot J_2 + S_6 \cdot \bar{C}_2 + KAD \cdot PU \cdot FML(C_1 + \bar{C}_2) + KAD \cdot PU \cdot \bar{C}_1 \cdot C_2$
$\$_5$ reset	$SP_2 + S_{17} + OVF + Kc + Kd + KCA$
(A)	$S_{10} \cdot \bar{C}_1 + SP_1 \cdot C_1 \cdot \bar{G} + SP_1 \cdot C_1 \cdot G + S_{21} + S_{20} + S_{17} + S_{15} + S_{12} \cdot \bar{C}_2 + S_{11} \cdot \bar{C}_2 \cdot \bar{J}_1 + S_{10} \cdot C_1 + S_7 \cdot \bar{C}_2 \cdot C_1 + S_6 \cdot \bar{C}_2 + S_2 \cdot C_2 + S_5 \cdot \bar{G} \cdot \bar{C}_1 + S_8 \cdot G \cdot \bar{C}_1 + S_3 + S_2 \cdot \bar{C}_1 + KCA + KCK$
(B)	$S_{14} + S_{16} + S_{13} \cdot \bar{C}_1 \cdot \bar{C}_2 + S_{12} \cdot \bar{C}_2 + S_7 \cdot \bar{C}_2 + S_6 \cdot \bar{C}_2 + S_5 \cdot G \cdot C_1 \cdot J_1 + S_9 \cdot \bar{G} \cdot J_1 \cdot C_1 + S_1 \cdot \bar{C}_1 \cdot \bar{C}_2 + KCA$
I	$S_{10} \cdot \bar{C}_1 + SP_2 + SP_1 \cdot C_1 \cdot \bar{G} + SP_1 \cdot C_1 \cdot G + S_{21} + S_{20} + S_{19} \cdot C_2 \cdot \bar{C}_1 \cdot \bar{J}_2 + S_{13} \cdot C_2 \cdot \bar{G} + S_{13} \cdot \bar{C}_1 \cdot \bar{C}_2 + S_{11} \cdot \bar{C}_2 \cdot \bar{J}_1 + S_5 \cdot G \cdot C_1 \cdot J_1 + S_5 \cdot \bar{G} \cdot \bar{C}_1 + S_5 \cdot \bar{C}_1 \cdot \bar{C}_2 + KCA + KCK$
II	$SP_2 + S_{21} + S_{19} \cdot \bar{C}_2 \cdot J_2 + S_{19} \cdot C_2 \cdot \bar{C}_1 \cdot \bar{J}_2 + S_{17} + S_{15} \cdot J_1 + S_{15} + S_{12} \cdot \bar{C}_2 + S_{11} \cdot \bar{C}_2 \cdot J_1 + S_{10} \cdot C_1 + S_9 \cdot J_2 \cdot \bar{C}_2 + S_8 + S_7 \cdot \bar{C}_2 \cdot C_1 + S_7 \cdot \bar{C}_2 + S_6 \cdot \bar{C}_2 + S_2 \cdot \bar{C}_1 + S_1 \cdot \bar{C}_1 \cdot \bar{C}_2 + KCA + KCK$
III	$SP_2 + SP_1 \cdot C_1 \cdot \bar{G} + S_{19} \cdot \bar{C}_2 \cdot J_2 + S_{19} \cdot C_2 \cdot \bar{C}_1 \cdot \bar{J}_2 + S_{17} + S_{15} \cdot J_1 + S_{15} + S_{14} + S_{16} + S_{11} \cdot \bar{C}_2 \cdot J_1 + S_{11} \cdot \bar{C}_2 \cdot \bar{J}_1 + S_{10} \cdot C_1 + S_6 \cdot \bar{C}_2 + S_8 \cdot C_2 + S_5 \cdot \bar{G} \cdot \bar{C}_1 + S_5 \cdot \bar{G} \cdot J_1 \cdot C_1 + S_2 \cdot \bar{C}_1 + KCA + KCK$

L.S.I. No. 2 - 1

$(\overline{T_4^{16}} + \overline{T_4^1}(K), \overline{T_4^{16}}(K), T_4^{16}, T_4^1(K), J_3, FA_1, FA_2, SE_1 \sim SE_4, K, OVF, FAS)$
 $(A_1 \text{ set}, B_1 \text{ set}, SD_1 \sim SD_4, t_1 \sim t_4, A_1 \sim A_4, A_{61} \sim A_{64})$

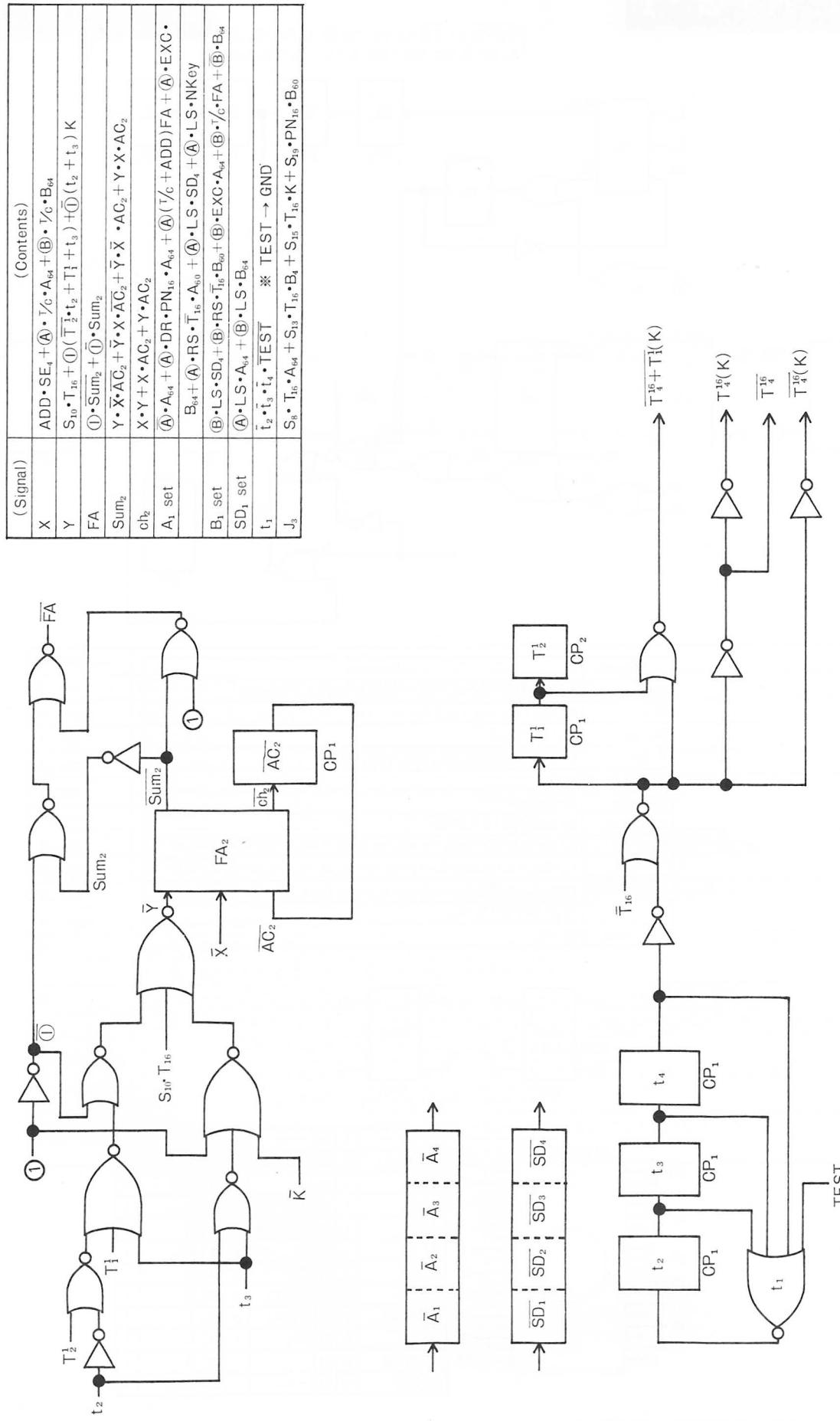


(Signal)	(Contents)
Sum ₁	$A_{60} \cdot \bar{B}_{60} \cdot \bar{AC}_1 + \bar{A}_{60} \cdot B_{60} \cdot \bar{AC}_1 + \bar{A}_{60} \cdot \bar{B}_{60} \cdot AC_1 + A_{60} \cdot B_{60} \cdot AC_1$
Ch ₁	$A_{60} \cdot B_{60} + A_{60} \cdot AC_1 + B_{60} \cdot AC_1$
AC ₁ set	$Ch_1 \cdot (\textcircled{3} + T_{16})$
K set	$K \cdot \bar{t}_1 + t_1 [AC_1 + SE_1(SE_2 + SE_3 + SE_4 \cdot AC_2)]$
FAS set	$S_6 \cdot \bar{C}_1 \cdot \bar{C}_2 \cdot \bar{K} \cdot T_4^{16}$
FAS reset	$S_6 \cdot C_1 \cdot \bar{C}_2 \cdot K \cdot T_4^{16} + \textcircled{7} \cdot \textcircled{B}$
OVF set	$S_1 \cdot T_{16} \cdot B_{64} + S_7 \cdot T_{16} \cdot B_4 + S_7 \cdot T_{16} \cdot A_4 + S_7 \cdot T_{16} \cdot A_{64} + S_3 \cdot T_{16} \cdot A_{64} + S_3 \cdot T_{16} \cdot \bar{B}_{64} + S_5 \cdot T_{16} \cdot AN_{16} + S_5 \cdot \textcircled{4} \cdot A_{64} \cdot \bar{C}_1 \cdot T_{16} + S_5 \cdot \textcircled{4} \cdot B_{64} \cdot C_1 \cdot T_{16} + S_6 \cdot C_1 \cdot \bar{C}_2 \cdot K \cdot FAS \cdot T_4^{16} + S_{10} \cdot C_1 \cdot \bar{P}N_{15} \cdot B_4 + S_{12} \cdot T_{16} \cdot A_{64} + S_{12} \cdot T_{16} \cdot A_4 + S_{12} \cdot T_{16} \cdot B_{64} + S_{20} \cdot PN_{16} \cdot T_{16} + SP_1 \cdot \textcircled{4} \cdot T_{16} \cdot A_{64}$
OVF reset	$\textcircled{7} \cdot \textcircled{B}$



(TOP VIEW)		I	$\overline{T_{16}}$	I	I3	$-V_{DD}$	I	25	CP_2	I
36	35	2	\overline{S}_1	I	I4	V_{SS} (GND)		26	\bar{B}_{60}	I
34	33	3	\overline{S}_2	I	I5			27	$\bar{P}N_{15}$	I
32	31	4	\overline{S}_3	I	I6	$\overline{T_4^{16}} + \overline{T_4^1}(K)$	0	28	\bar{A}_{60}	I
30	29	5	\overline{S}_4	I	I7	TEST \ddagger		29	\bar{B}_4	I
28	27	6	\overline{S}_5	I	I8	\bar{t}_4	0	30	\bar{AN}_{16}	I
26	25	7	\bar{C}_1	I	I9	$\overline{T_4^{16}}$	0	31	\textcircled{A}	I
24	23	8	\bar{C}_2	I	I20	$T_4^{16}(K)$	0	32	\textcircled{B}	I
22	21	9	$\overline{A_1 \text{ set}}$	0	I21	$\overline{T_4^{16}}(K)$	0	33	\bar{I}	I
20	19	10	\overline{J}_3	0	I22	CP_1	I	34	\bar{II}	I
		11	$\overline{B_1 \text{ set}}$	0	I23			35	\bar{III}	I
		12	OVF	0	I24	$-V_{SS}$		36	\bar{NKey}	I

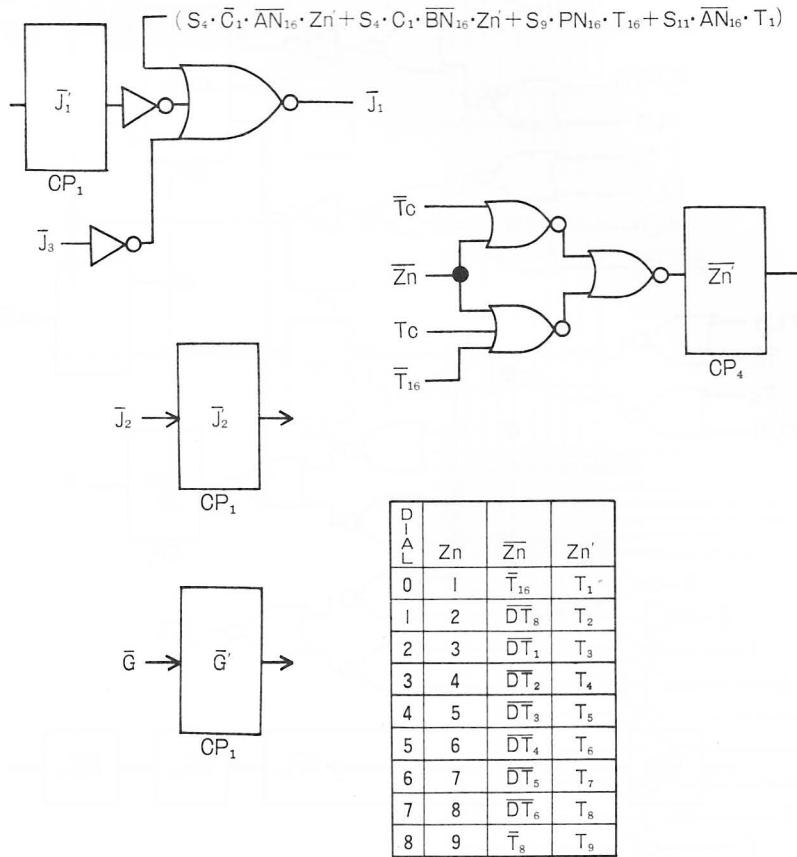
L.S.I. No. 2 - 2



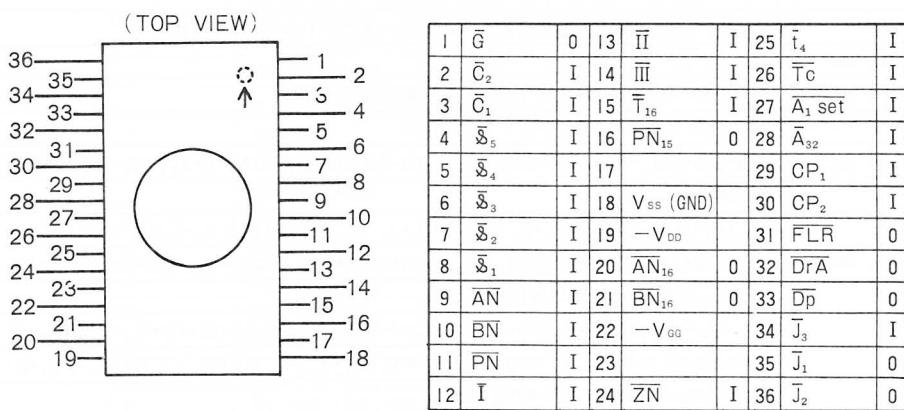
L.S.I. No. 3 - 1

$$(J_1, J_2, G, ZN, AN_1 \sim AN_{17}, CP_4, FLR, DP, DrA)$$

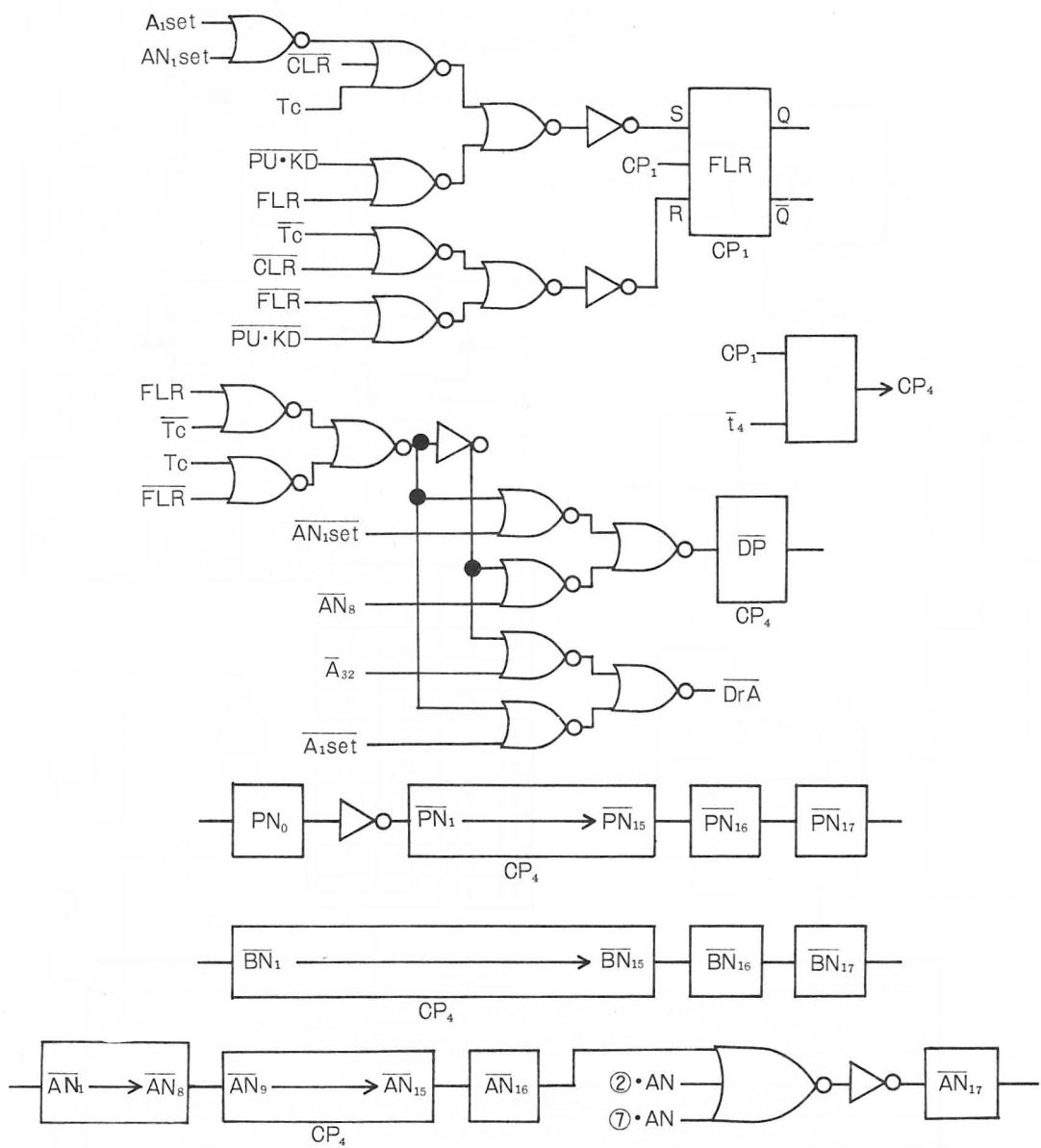
$$(PN_0 \sim PN_{17}, BN_1 \sim BN_{17})$$



(Signal)	(Contents)
J'_1 set	$(S_5 + S_8) \cdot J_1 \cdot \bar{T}_4 + J_1 (S_4 + S_{11} + S_{13}) + J_1 (S_{18} + S_{19} + S_{20})$
J_3	$S_8 \cdot T_{16} \cdot A_{64} + S_{13} \cdot T_{16} \cdot B_4 + S_{15} \cdot T_{16} \cdot K + S_{19} \cdot PN_{16} \cdot B_{60}$
J'_2 set	J_2
J_2	$S_8 \cdot AN_{16} \cdot \bar{AN}_{15} \cdot \bar{J}_1 \cdot T_{16} + S_{18} \cdot BN_{16} \cdot AN_{16} \cdot \bar{C}_2 + S_{18} \cdot BN_{16} \cdot \bar{AN}_{16} \cdot C_2 + SP_1 \cdot AN_{16} \cdot Zn'$ $+ S_{21} \cdot AN_{16} \cdot AN_{15} \cdot T_{16} + J'_2 (S_9 + S_{18} + S_{19}) + SP_1 \cdot J'_2$
G' set	G
G	$S_4 \cdot Zn' + SP_1 \cdot \bar{C}_1 \cdot Zn' + S_{13} \cdot \bar{C}_2 \cdot BN_{16} + S_5 \cdot G' + S_{13} \cdot C_2 \cdot G' + SP_1 \cdot C_1 \cdot G'$ $+ S_4 \cdot C_1 \cdot \bar{BN}_{16} \cdot G' + (S_4 + SP_1) \bar{C}_1 \cdot \bar{AN}_{16} \cdot G' + S_{13} \cdot \bar{C}_2 \cdot \bar{AN}_{16} \cdot G'$



L.S.I. No. 3 - 2

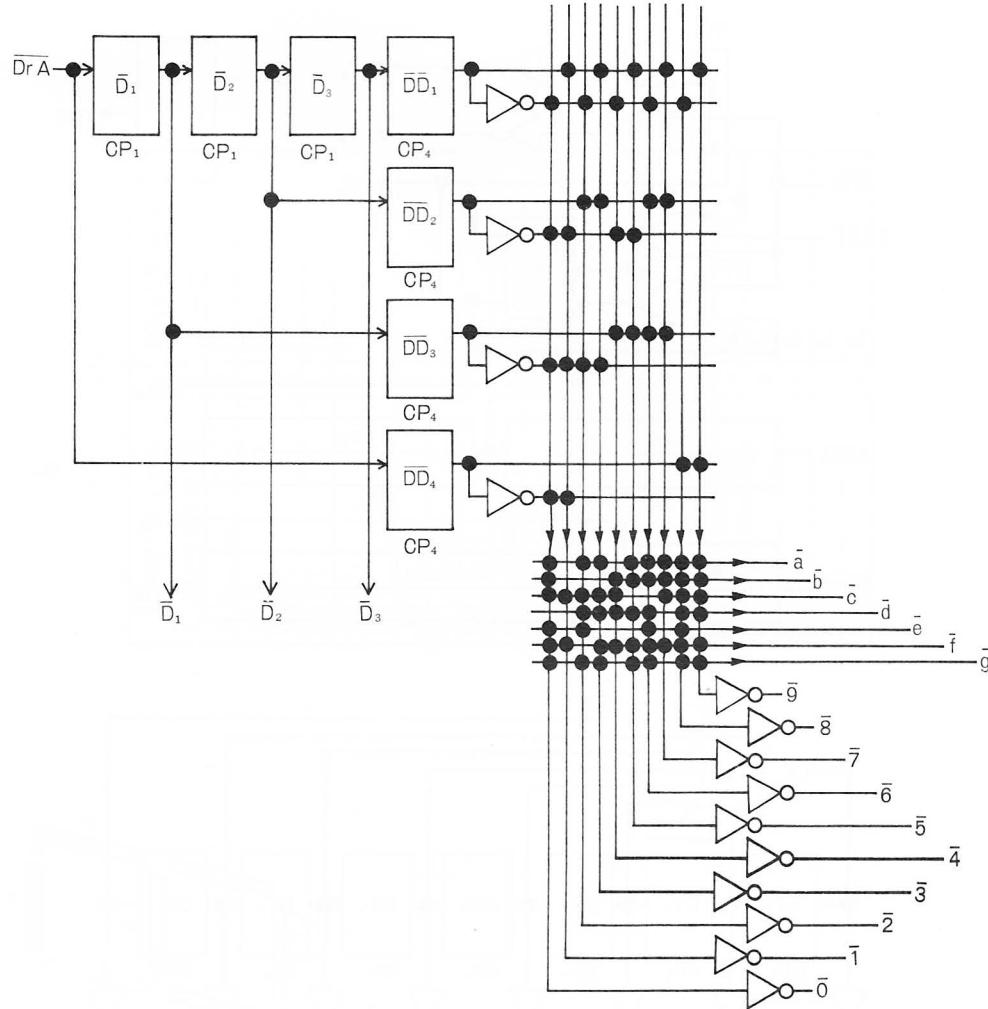


(Signal)	(Contents)
FLR set	$PU \cdot KD \cdot \overline{FLR} + \overline{T_c} \cdot CLR \cdot (A_1\text{set} + AN_1\text{set})$
FLR reset	$T_c \cdot CLR + FLR \cdot PU \cdot KD$
DP set	$AN_1\text{set} \cdot (\overline{FLR} \cdot T_c + FLR \cdot \overline{T_c}) + AN_8 \cdot (FLR + \overline{T_c}) \cdot (\overline{FLR} + T_c)$
DrA	$A_{32} \cdot (FLR + \overline{T_c}) \cdot (\overline{FLR} + T_c) + A_1\text{set} \cdot (FLR \cdot T_c) + (FLR \cdot \overline{T_c})$
PN ₀ set	$PN \cdot J_1 \cdot Ig \cdot T_{16}$
PN ₁ set	$PN \cdot LS \cdot PN_{17} + PN \cdot Ig \cdot T_1 + PN_0 + PN_{16} \cdot (\overline{CLR} + \overline{PN})$
PN	$S_{10} \cdot \bar{C}_1 + SP_2 + S_{20} + S_{15} \cdot J_1 + S_8 + KCA$
BN ₁ set	$BN \cdot LS \cdot BN_{17} + BN \cdot RS \cdot BN_{15} \cdot \bar{T}_{16} + BN \cdot EXC \cdot AN_{16} + BN \cdot Ig \cdot T_1 + BN \cdot LS \cdot BN_{16} \cdot T_{16}$ + $\overline{BN} \cdot BN_{16}$
BN	$S_{13} \cdot \bar{C}_1 \cdot \bar{C}_2 + S_7 \cdot \bar{C}_2 + S_6 \cdot \bar{C}_2 + S_5 \cdot G \cdot C_1 \cdot J_1 + S_5 \cdot \bar{G} \cdot J_1 \cdot C_1 + S_1 \cdot \bar{C}_1 \cdot \bar{C}_2 + KCA$
AN ₁ set	$AN \cdot LS \cdot AN_{17} + AN \cdot RS \cdot AN_{15} \cdot \bar{T}_{16} + AN \cdot EXC \cdot BN_{16} + AN \cdot Ig \cdot T_1 + AN \cdot LS \cdot AN_{16}$ + $T_{16} + \overline{AN} \cdot AN_{16} + AN \cdot Ig \cdot T_{16}$
AN	$S_{10} \cdot \bar{C}_1 + SP_1 \cdot C_1 \cdot \bar{G} + SP_1 \cdot C_1 \cdot G + S_{20} + S_{19} \cdot \bar{C}_2 \cdot J_2 + S_{19} \cdot C_2 \cdot \bar{C}_1 \cdot \bar{J}_2 + S_{13} \cdot C_2 \cdot \bar{G}$ + $S_{12} \cdot \bar{C}_2 + S_{11} \cdot \bar{C}_2 \cdot J_1 + S_{11} \cdot \bar{C}_2 \cdot \bar{J}_1 + S_9 \cdot \bar{C}_2 \cdot J_2 + S_7 \cdot \bar{C}_2 \cdot C_1 + S_5 \cdot \bar{G} \cdot \bar{C}_1 + S_5 \cdot G \cdot \bar{C}_1$ + $S_3 \cdot C_3 + S_2 \cdot \bar{C}_1 + KCA + KCK$

L.S.I. No. 4 - 1

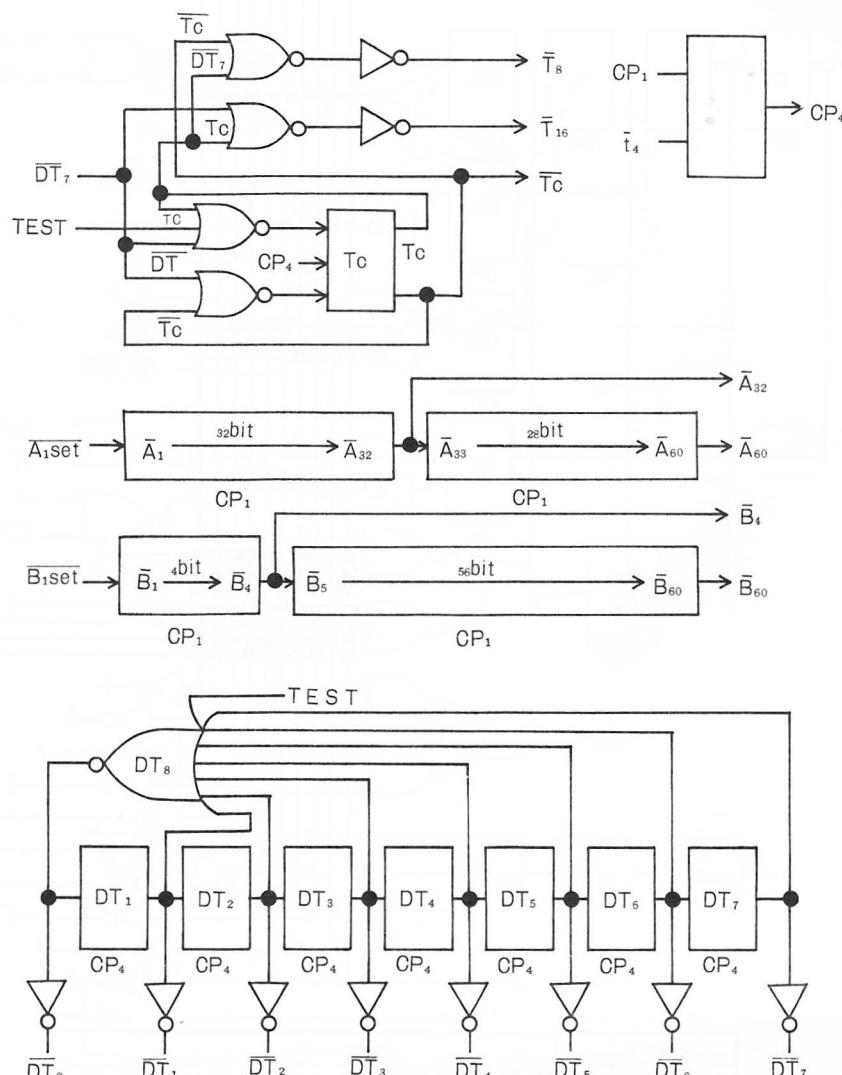
$$\left(D_1 \sim D_3, DT_1 \sim DT_8, A_1 \sim A_{60}, B_1 \sim B_{60}, TC, \bar{T}_8, \bar{T}_{16} \right)$$

$\left(DD_1 \sim DD_4, 0 \sim 9, CP_4 \right)$

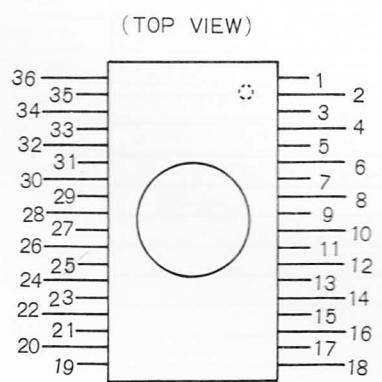


(Signal)	(Contents)
a	$\overline{DD}_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + \overline{DD}_1 \cdot DD_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3$ + $\overline{DD}_1 \cdot DD_2 \cdot DD_3 + DD_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
b	$DD_1 \cdot DD_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + \overline{DD}_1 \cdot \overline{DD}_2 \cdot DD_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_2 \cdot DD_3$ + $DD_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
c	$\overline{DD}_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + \overline{DD}_1 \cdot DD_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3$ + $\overline{DD}_1 \cdot DD_2 \cdot DD_3 + DD_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
d	$\overline{DD}_1 \cdot DD_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 + \overline{DD}_1 \cdot \overline{DD}_2 \cdot DD_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3 +$ $\overline{DD}_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
e	$\overline{DD}_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + \overline{DD}_1 \cdot DD_2 \cdot \overline{DD}_3 + \overline{DD}_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4$
f	$DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + DD_1 \cdot DD_2 \cdot \overline{DD}_3 + \overline{DD}_1 \cdot \overline{DD}_2 \cdot DD_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3 +$ $\overline{DD}_2 \cdot DD_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3 + DD_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
g	$\overline{DD}_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 \cdot \overline{DD}_4 + \overline{DD}_1 \cdot DD_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot \overline{DD}_3 + DD_1 \cdot \overline{DD}_2 \cdot DD_3$ + $\overline{DD}_1 \cdot DD_2 \cdot DD_3 + \overline{DD}_1 \cdot DD_4 + DD_1 \cdot DD_4$
9	$DD_1 \cdot DD_4$
8	$\overline{DD}_1 \cdot DD_4$
7	$DD_1 \cdot DD_2 \cdot DD_3$
6	$DD_1 \cdot DD_2 \cdot DD_3$
5	$DD_1 \cdot \overline{DD}_2 \cdot DD_3$
4	$\overline{DD}_1 \cdot DD_2 \cdot DD_3$
3	$DD_1 \cdot DD_2 \cdot \overline{DD}_3$
2	$DD_1 \cdot DD_2 \cdot \overline{DD}_3$
1	$DD_1 \cdot DD_2 \cdot DD_3$
0	$DD_1 \cdot DD_2 \cdot \overline{DD}_3 \cdot DD_4$

L.S.I. No. 4 - 2

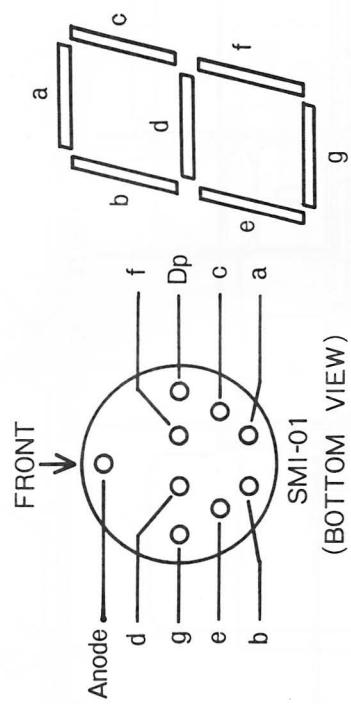


(Signal)	(Contents)
TC set	$\bar{TC} \cdot DT_7 \cdot TEST$
TC reset	$DT_7 \cdot TC$
T_8	$TC \cdot DT_7$
T_{16}	$TC \cdot DT_7$

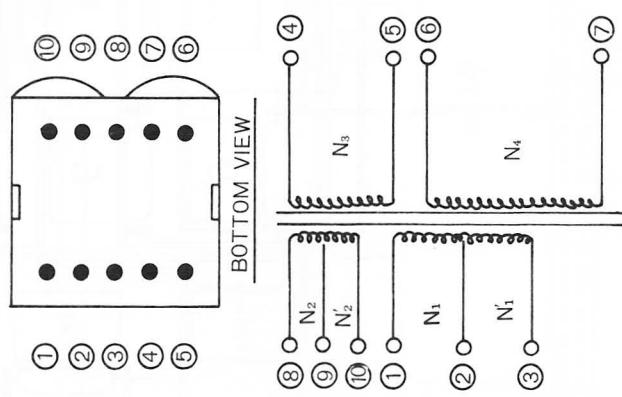


I	$\bar{5}$ or \bar{e}	0	I3	$B_1\text{ set}$	I	25	TEST *	
2	$\bar{6}$ or \bar{d}	0	14	\bar{B}_{60}	0	26	\bar{T}_{16}	0
3	$\bar{7}$ or \bar{c}	0	15	\bar{A}_{32}	0	27	\bar{T}_8	0
4	$\bar{8}$ or \bar{b}	0	16	\bar{DT}_7	0	28	\bar{TC}	0
5	$\bar{9}$ or \bar{a}	0	17	\bar{DT}_6	0	29	\bar{t}_4	I
6	$\bar{A}_1\text{ set}$	I	18	\bar{DT}_5	0	30	\bar{DrA}	I
7	CP_2	I	19	\bar{DT}_4	0	31		
8	CP_1	I	20	\bar{DT}_3	0	32	\bar{D}_1 or \bar{D}_3	0
9	$-V_{ss}$ (GND)		21	\bar{DT}_2	0	33	\bar{D}_1 or \bar{D}_2	0
10	V_{ss} (GND)		22	\bar{DT}_1	0	34	\bar{D}_2 or \bar{D}_1	0
11	$-V_{ss}$		23	\bar{DT}_8	0	35	\bar{D}_3 or \bar{g}	0
12	\bar{B}_4	0	24	\bar{A}_{60}	0	36	$\bar{4}$ or \bar{i}	0

Pin Assignment

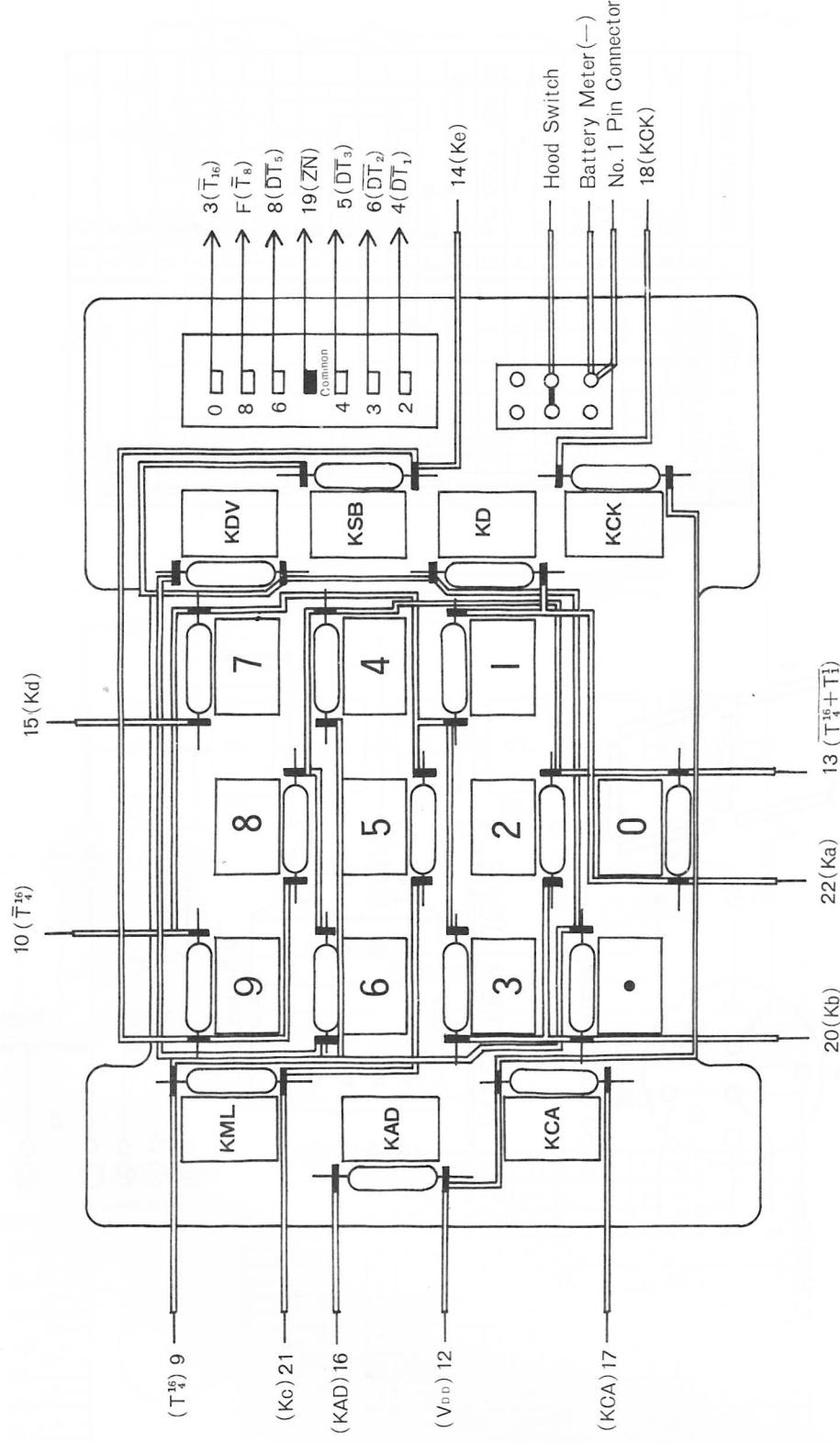


INVERTER CORE

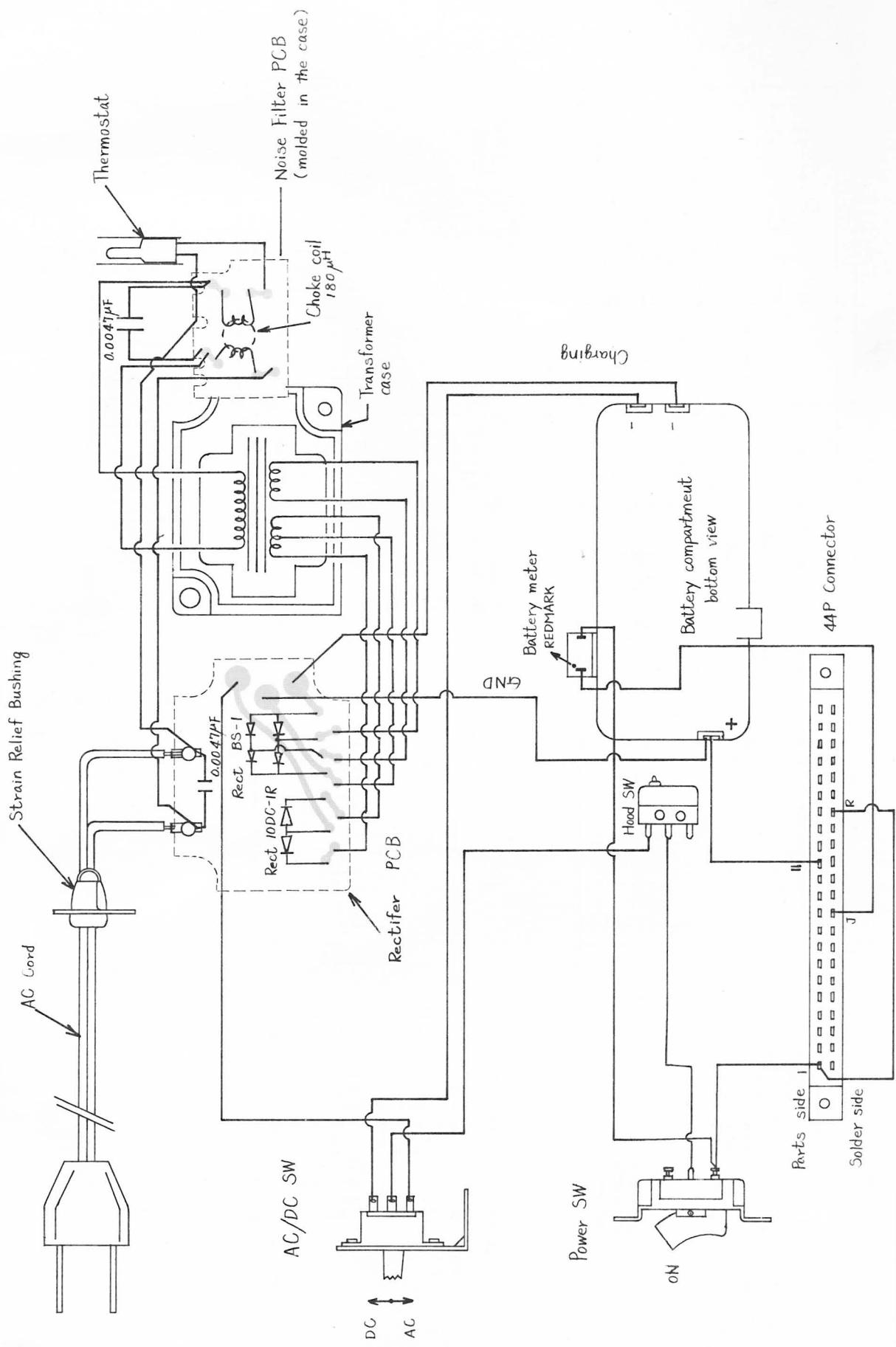


	PARTS SIDE	SOLDER SIDE
1	POWER•SWITCH	A
2	\bar{T}_{16}	$D\bar{T}_4$
3	$D\bar{T}_1$	$C\bar{T}_8$
4	$D\bar{T}_3$	CP_2
5	$D\bar{T}_2$	$E\bar{T}_6$
6	$D\bar{T}_5$	$F\bar{T}_8$
7		H
8		J BATTERY METER
9	$T_4^{16}(K)$	K DT_i
10	$\bar{T}_4^{16}(K)$	L CP_1
11	V_{SS} (GND)	M \bar{T}_C
12	$V_{DD}(-5V)$	N OVF
13	$\bar{T}_4^{16} + T_1(K)$	P DrA
14	Ke	R Power SW
15	Kd	S \bar{L}_4
16	KAD	T FLR
17	KCA	U \bar{DP}
18	KCK	V \bar{S}_4
19	$Z\bar{n}$	W \bar{S}_2
20	Kb	X \bar{S}_1
21	Kc	Y \bar{S}_3
22	Ka	Z \bar{S}_5

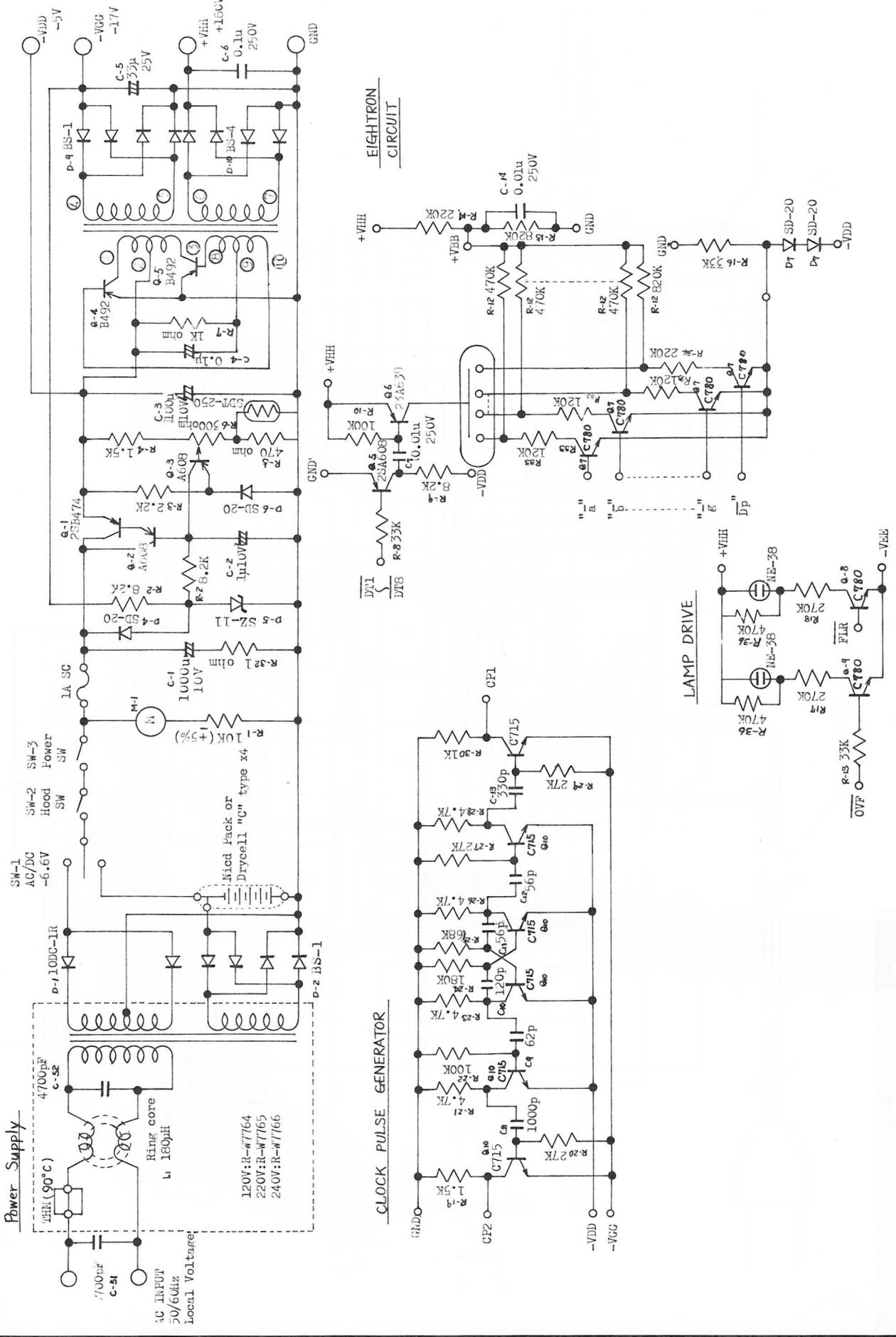
Key Board Connection



Power Supply Wiring



Schematic





S1 Printed in Japan